

PROCESS CONDITION SENSING WAFER AND DATA ANALYSIS SYSTEM

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BACKGROUND OF THE INVENTION

Field of the Invention

[0001] The present invention relates to a semiconductor wafer processing, LCD display glass substrate processing, magnetic memory disc processing and other devices fabricated from thin film processes and more specifically to a substrate which can sense and transmit processing conditions.

Discussion of the Related Art

[0002] The fabrication of an integrated circuit, display or disc memory generally employs numerous processing steps. Each process step must be carefully monitored in order to provide an operational device. Throughout the imaging process, deposition and growth process, etching and masking process, etc., it is critical, for example, that temperature, gas flow, vacuum pressure, chemical gas or plasma composition and exposure distance be carefully controlled during each step. Careful attention to the various processing conditions involved in each step is a requirement of optimal semiconductor or thin film processes. Any deviation from optimal processing conditions may cause the ensuing integrated circuit or device to perform at a substandard level or, worse yet, fail completely.

[0003] Within a processing chamber, processing conditions vary. The variations in processing conditions such as temperature, gas flow rate and/or gas composition greatly affect the formation and thus the performance of the integrated circuit. Using a substrate to measure the processing conditions that is of the same or similar material as the integrated circuit or other device provides the most accurate measure of the conditions because the thermal conductivity of the substrate is the same as the actual circuits that will be processed. Gradients and variations exist throughout the chamber for virtually all

process conditions. These gradients therefore also exist across the surface of a substrate. In order to precisely control processing conditions at the wafer, it is critical that measurements be taken upon the wafer and the readings be available in real time to an automated control system or operator so that the optimization of the chamber processing conditions can be readily achieved. Processing conditions include any parameter used to control semiconductor or other device manufacture or any condition a manufacturer would desire to monitor.

[0004] Within the processing chamber a robot head transports the test wafer or substrate. One example of a device incorporating a robot head is manufactured by the TEL Corporation. The robot head can pivot. The robot head also incorporates multiple levels or hands. A first level or hand can extend out and a second level or hand can further extend out carrying a wafer. A second robot or moving platform can receive the wafer and extend it to a third holder which lowers it into the process chamber. For more information about the robot head and processing chamber, please refer to U.S. Patent No. 5,564,889 to Araki, entitled "Semiconductor Treatment System and Method for Exchanging and Treating Substrate," which is hereby incorporated by this reference in its entirety.

SUMMARY OF THE INVENTION

[0005] Placement of sensors directly upon or in the substrate and spaced throughout the substrate surface yields an accurate gradient reading of various processing conditions upon the surface of the wafer. The processing conditions may be stored in memory for later evaluation or are available in real time to be read via a remote data processing device such as a computer, PDA or any other microprocessor controlled device that can present information and receive input from a control system or operator. The operator can monitor the processing conditions in real time, and then alter the settings of the processing chamber to reach an ideal state while continuing to monitor the effects, or this may be accomplished by an automated optimization and control system. Additionally, subsequent processing steps can be instantaneously modified based on a process condition of a prior step.

[0006] Distancing sensitive electronic circuitry from the processing conditions results in a wider operating range and more accurate, reliable, repeatable and drift-free operation.

BRIEF DESCRIPTION OF THE FIGURES

- [0007] FIG. 1A is a perspective view of PCMD 100, a first embodiment of the invention, in an extended state.
- [0008] FIG. 1B is a perspective view of PCMD 100 in a concentric state.
- [0009] FIG. 1C is a top view of a processing chamber and robot hand.
- [0010] FIG. 1D is a top view of a robot hand extending.
- [0011] FIG. 1E is a plan view of a robot hand extending.
- [0012] FIG. 1F is a schematic of the electronics and circuitry common to all embodiments.
- [0013] FIG. 1G is a cross section of substrate 104.
- [0014] FIG. 1H is a top view of substrate 104.
- [0015] FIG. 1J is a perspective view of a sensor in substrate 104.
- [0016] FIG. 2 is a perspective view of PCMD 200, another embodiment of the invention.
- [0017] FIG. 3A is a perspective view of PCMD 300, another embodiment of the invention.
- [0018] FIG. 3B is a plan view of PCMD 300.
- [0019] FIG. 4 is a perspective view of PCMD 400, another embodiment of the invention.
- [0020] FIG. 5 is a perspective view of PCMD 500, another embodiment of the invention.
- [0021] FIG. 6 is a perspective view of PCMD 600, another embodiment of the invention.
- [0022] FIG. 7 is a perspective view of PCMD 700, another embodiment of the invention.

DETAILED DESCRIPTION OF THE INVENTION

[0023] The measurement system of the present invention measures processing conditions in various locations of a wafer or substrate and transmits them in real time to a data processing device or records them in memory for later transmission or downloading of process conditions.

[0024] As defined herein, "processing conditions" refer to various processing parameters used in manufacturing an integrated circuit. Processing conditions include any parameter used to control semiconductor manufacture or any condition a manufacturer would desire to monitor such as but not limited to temperature, processing chamber pressure, gas flow rate within the chamber, gaseous chemical composition within the chamber, ion current density, ion current energy, light energy density, and vibration and acceleration of the wafer.

[0025] The invention will now be described with reference to the figures.

[0026] Figure 1A illustrates processing condition measuring device ("PCMD")100, a first embodiment of the invention, in an extended position. PCMD 100 has two principal pieces, substrate 104 and electronics disc 106. The substrate 104 is used to measure the processing conditions of semiconductor manufacturing equipment, glass substrate processing equipment, and magnetic memory disc processing equipment. Specifically, it is used to measure the conditions that a wafer or substrate undergoes during processing. Sensors are arranged in different areas on the surface or within substrate 104 in order to measure the processing conditions across the substrate. By measuring in different areas of the substrate, the gradient across the substrate can be calculated, and additionally, the condition at a particular location of the substrate can be correlated to the resultant characteristics of the substrate. The number of sensors in/on substrate 104 will vary upon the processing condition being measured and the size of substrate 104. In one embodiment for measuring temperature, a 200mm diameter substrate has 17 sensors whereas a 300mm diameter substrate has 29 sensors. Substrate 104 will be discussed later in more detail with regard to Figures 1G-1H.

[0027] Electronics disc 106 is connected to substrate 104 by cable 108. Cable 108 can be any type of cable but is preferably a flat ribbon type cable that is flexible and has a low profile. The processing conditions that PCMD will be subjected to often involve high or variable temperatures or other conditions, both of which negatively affect the

functionality, accuracy, and reliability of electronic components. Furthermore, numerous other processing steps and conditions make it advantageous to distance the electronics from the process or even to locate the electronics outside of the processing environment. Separating the PCMD into two pieces, in this embodiment, allows the substrate and the sensors to be inside of the processing chamber while the electronics can remain outside of the chamber free from the deleterious effects of the elevated temperature and the other various processing conditions. Cable 108 of PCMD 100 thus may pass from the exterior to the interior of the processing chamber under a chamber seal to allow the process to proceed without risk of leakage of the process atmosphere to or from the exterior environment. The cable is preferably made of a material resistant to temperature and other gaseous chemicals used in the manufacturing process such as polyimide.

[0028] Data processing device ("DPD") 110 is connected to electronics disc 106 with telecommunications link 112 at data port 114. Telecommunications link 112 may be a wired or wireless link and will be described later in further detail with regard to Figure 1F.

[0029] Figure 1B shows substrate 104 and electronics disc 106 concentrically located with cable 108 between them. Concentric is hereby defined as the circumference of one circle being within the circumference of another circle, the circles not necessarily having the same center. Thus, this definition also encompasses eccentricity of the circles.

[0030] Concentrically located, they can be loaded into two robot arms spaced one above the other. The substrate 104 with the sensors would be held by the lower robot arm. The lower arm may be extended for insertion of the substrate 104 into a processing chamber. The processing chamber has three areas for insertion: area 134, area 136, and area 138. Figure 1C illustrates a robot head 130 inserting PCMD 100 into various areas of a processing chamber 132. Robot hands 130a (above, holding the electronics disc 106) and robot hand 130b (below, holding the substrate 104) are both capable of independently extending. Figures 1D and 1E illustrate three hands 130a, 130b, and 130c of robot head 130. In Figure 1D, level robot hand 130b is extended away from robot hand 130a of robot head 130. Level 130b or 130c would contain substrate 104, and level 130a or 130b, respectively, would contain electronics disc 106 when PCMD 100 is in its extended state. The robot head 130 would first have PCMD in its concentric state as seen in Figure 1B as it approaches the chamber. Robot hand 130b would then extend away from hand 130a and thus separate substrate 104 from electronics disc 106. In this way, substrate 104 can

be placed into area 134 as seen in Figure 1C. If PCMD were to be placed into area 136, robot hands 130a and 130b would be inserted into area 136 with PCMD 100 in its concentric state. The substrate 104 would be lowered onto a sliding platform which would move substrate 104 to the process chamber at position 136. Prior to loading into robot hands 130a and 130b, PCMD 100 would be rotated to a proper orientation so that it could be extended along the axis of cable 108. Placement of substrate 104 into process chamber 138 would be substantially the same as for chamber 136 except for a different rotation angle to allow the extension along the axis of cable 108 to be in line with chamber 138.

[0031] Figure 1F is a block diagram illustrating the electrical circuits and signal flow of PCMD circuitry 151 and DPD 110, which is common to all the embodiments of the invention. Sensors 150, as mentioned previously, are in or on substrate 104. The output of sensors 150 is coupled to SAC 154 via conductors 153. Memory 152 is optional and is preferably located near sensors 150 either on substrate 104 or on a connector of cable 108. Memory 152, if present, stores digital sensor data which is passed through SAC 154 without processing and continues through conductors 156 to DTC 158 for reading of the digital sensor data by the micro-controller 158B. Memory 152 may contain calibration coefficients for the sensors 150. In this way, even if the electronics disc 106 is changed, memory 152 and the calibration coefficients will remain with the appropriate sensors 150. SAC 154 is preferably located at electronics disc 106 but may be located at substrate 104 or anywhere within or external of the process chamber. SAC 154 contains the circuitry necessary to pick up the sensor outputs and if necessary provide any input power or other signals needed to drive the sensors such as amplifiers, current sources, and filters. SAC 154 drives the signal to data transmission circuitry ("DTC") 158 over conductors 156. Power supply 162 can be a storage cell, radiative energy conversion cell, or inductive coupled power source and powers all the components of PCMD 100 via electrical bus 164.

[0032] DTC 158 comprises the circuitry necessary to process, store and transmit the signals in analog or digital form from SAC 154 to DPD 110 over data link 112. In the case that the signals are sent digitally, DTC 158 may include one or more analog to digital converters 158A. A transceiver 158C within DTC 158 sends and receives the measured processing conditions and any control signals to and from transceiver 110d of DPD 110. Although transceiver 110d is shown as part of DPD 110, it may also be remotely located on robot head 130. DTC 158 may also contain calibration coefficients

for the sensors 150. DTC 158 may read the calibration coefficient information and communicate it to data processing device 110 for applying the calibration correction to the measured data. DTC 158 may also optionally contain memory 158D to store the recorded processing conditions as measured by sensors 150 in the raw or corrected state, as well as other information such as the calibration coefficients. Microcontroller or gate array 158B manages the processes of DTC 158. Data link 112 may be a wireless link or may be a multi conductor data cable such as an RS 232 or universal serial bus (USB) connection. In the case that data link 112 is wireless, the transceivers 158c and 110d can communicate with infrared, acoustic, sonic, ultrasonic, or radio frequency signals. Any number of well-known protocols may be employed such as Bluetooth. The transceiver may also send and receive signals inductively. In PCMD 100, DTC 158 is a part of electronics disc 106, whereas in the following embodiments it may be located elsewhere. For clarity, interconnects or wiring within SAC 154, DTC 158 and DPD 110 have not been shown.

[0033] Data processing device 110 can be any microprocessor or gate array controlled device such as a computer or a personal digital assistant ("PDA") or a purpose built computer. DPD 110 includes a central processing unit 110A and may also include input/ output devices 110B such as a display or keyboard, mouse etc..., memory 110C, and transceiver 110D.

[0034] Substrate 104 has a base layer 140 that is preferably a silicon wafer, but can also be made out of numerous other materials that may be used in manufacturing integrated circuits or thin film devices including glass, ceramic, GaAs, carbide or nitride. Substrate 104 and electronics disc 106 are preferably 200mm or 300mm in diameter to simulate the size of current wafers and in order to be handled by conventional wafer handling machinery; however, they may be of any diameter or any shape.

[0035] Figure 1G is a cross section of substrate 104. In this illustrative example, base layer 140 is a silicon wafer with various layers formed upon the wafer. Base layer 140 has an insulating layer 142 upon base layer 140. Insulating layer 142 can be any insulative material but is preferably a thermal oxide such as silicon dioxide. A cap layer 144 is then formed on the top of insulating layer 142. Cap layer 144 compensates for any defects in insulating layer 142. Upon cap layer 144 is interconnect layer 146. Interconnect layer 146 is a conductive layer that is used to transfer the signals to and from the sensors monitoring the process conditions. Interconnect layer 146 is etched to form

circuit traces leading to and from the precise location of the sensors, and any bond pads needed for interconnection. Additionally, the sensors themselves may be formed within interconnect layer 146, and within other conductive layers (not shown). Upon interconnect layer 146 is passivation layer 148. Passivation layer 148 is preferably a nitride layer but can be any type of dielectric material. Figure 1H illustrates the preferred layout of sensors 150 on/in substrate 104, although many different layouts are possible and within the scope of the invention. Figure 1J illustrates a discrete sensor 150 mounted in substrate 104 and connected to circuit traces formed in interconnect layer 146. A thermally conductive insulating ceramic material (not shown) covers sensor 150 and fills cavity 152. For more information on the sensors and interconnects made in a thin film layer directly deposited on the substrate, please refer to U.S. Patent No. 6,190,040 B1 to Renken et al., entitled "Apparatus for Sensing Temperature on a Substrate in an Integrated Circuit Fabrication Tool," which is hereby incorporated by this reference in its entirety.

[0036] Sensors 150 are necessary for detecting various processing conditions are mounted on or fabricated in substrate 104 according to well known semiconductor transducer design. For measuring temperature, a popular transducer is an RTD or thermistor, which includes a thin-film resistor material having a temperature coefficient. A magneto-resistive material may also be used to measure the amount of magnetic flux exerted upon substrate 104. A resistance-to-voltage converter is often formed within the substrate between distal ends of the resistive-sensitive material (either thermistor or magneto-resistive material). Another exemplary temperature sensor includes a thermocouple made of two dissimilar conductors lithographically formed in the layers of the substrate. When the junction between the conductors is heated, a small thermoelectric voltage is produced which increases approximately linearly with junction temperature. Another example of a temperature sensor includes a diode that produces a voltage that increases with temperature. By connecting the diode between a positive supply and a load resistor, current-to-voltage conversion can be obtained from the load resistor. Another sensor is a piezoelectric device such as a quartz tuning fork fabricated from quartz crystal cut on a crystal orientation which exhibits a temperature dependent frequency of oscillation. The sensor's oscillating frequency can be referenced against a master oscillator formed by a piezoelectric device such as a quartz tuning fork which is fabricated from a crystal orientated to minimize frequency change with temperature. The frequency difference between the sensor and master oscillator would provide a direct

digital temperature dependent signal. Piezoelectric sensors may also be used to sense mass change to measure deposition mass and rates or other process conditions.

[0037] Sensors 150 may also be used to measure pressure, force or strain at select regions across substrate 104, either as a discrete sensor or a sensor integrally formed in the layers of substrate 104. There are many types of pressure transducers capable of measuring the atmospheric pressure exerted upon the wafer. A suitable pressure transducer includes a diaphragm-type transducer, wherein a diaphragm or elastic element senses pressure and produces a corresponding strain or deflection which can then be read by a bridge circuit connected to the diaphragm or cavity behind the diaphragm. Another suitable pressure transducer may include a piezoresistive material placed within the semiconductor substrate of substrate 104. The piezoresistive material is formed by diffusing doping compounds into the substrate. The resulting piezoresistive material produces output current proportional to the amount of pressure or strain exerted thereupon.

[0038] Sensors 150 may also be used to measure flow rate across substrate 104. In addition, humidity and moisture sensors can also be formed upon substrate 104. A well-known method for measuring flow rate, a hot-wire anemometer, may be incorporated into substrate 104. Fluid velocity is based upon the frequency of vortex production as a streamlined fluidic flow strikes a non-streamlined obstacle formed upon substrate 104. Measurement of fluid flow generally involves the formation of special vortices on either side of the obstacle. Thus, an alternating pressure difference occurs between the two sides. Above a threshold (below which no vortex production occurs), the frequency is proportional to fluid velocity. Of many methods of detecting the alternating pressure difference, a hot thermistor is preferably placed in a small channel between the two sides of the obstacle. The alternating directions of flow through the capitalized channel periodically cool the self-heated thermistor thereby producing an AC signal and corresponding electric pulses at twice the vortex frequency. Therefore, an obstacle protruding from substrate 104 in front of a thermistor can provide solid-state flow rate measurement. Heat can be transferred between self heated thermistors placed in close proximity to each other. Fluid flow transfers thermal energy between the adjacent thermistors causing a thermal imbalance proportional to mass flow. Two or more adjacent sensors can be arrayed to measure flow along a vector, or multiple flow vectors may also be sensed. The thermal imbalance can be detected to produce a DC signal related to mass flow. Flows in multiple directions can be compared to detect flow

[0039] Sensors 150 can also be used to measure the gaseous chemical concentration placed upon substrate 104. Chemical composition sensors utilize a membrane which is permeable to specific ions to be measured. Ideally, the membrane should be completely impermeable to all other ions. The conductivity of the membrane is directly proportional to the transport of select ions which have permeated the membrane. Given the variability of membrane conductivity, measurements can be taken which directly correlate to the amount of chemical ions present within the ambient surrounding substrate 104.

[0040] Sensors 150 may also be used to measure ion current density and ion current energy with a parallel plate structure, an array of collecting plates, and collecting plates with control grids supported above the collecting plates. The current flowing between parallel plates, or to the array of collecting plates will increase with ion current density. Ion current energy can be detected by applying a constant or varying DC potential on the grids above the plates. This will modulate current flow with ion current energy allowing the energy distribution to be detected. This is useful in monitoring and regulating a deposition or etching process.

[0041] A piezoelectric transducer/sensor may also be integrated into substrate 104 to measure the resonant frequency of a layer and thus the mass or thickness of the layer.

[0042] Additionally, sensors 150 can also be used to detect a change in position or displacement of an object spaced from substrate 104. Exemplary displacement transducers include electro-optical devices which can measure photon energy (or intensity) and convert photon energy to an electric field or voltage. Relatively well known electro-optical devices include light-emitting diodes, photodiodes, phototransistors, etc., which can be formed upon a semiconductor substrate. Displacement sensors are used to provide accurate information about electrode spacing within an etch or deposition chamber, and can also provide spacing information between a wafer and corresponding masks and/or radiation source.

[0043] Figure 2 illustrates another embodiment of a process condition measuring device, PCMD 200. PCMD 200 is similar to PCMD 100 except that electronics disc 206 is smaller than electronics disc 106 of PCMD 100 and substrate 104. As in PCMD 100, the electronics disc 206 is separated from substrate 104 in an extended position. PCMD 200 may come together with (above or below) substrate 104 or may always remain

extended. Thus the electronics can be distanced from the deleterious conditions of the processing chamber. The electronics may be in a form factor other than a disc.

[0044] Figure 3A illustrates yet another embodiment of a process condition measuring device, PCMD 300. PCMD 300 is similar to PCMD 200 of Figure 2 but includes an additional electronics platform 207 upon the surface, into the surface or into a cavity of substrate 104. The electronic and power supply circuitry 151 previously contained in electronics disc 106 and 206 of PCMD 100 and 200 is now divided between electronics disc 206 and electronics platform 207. Any portion of PCMD circuitry 151 shown in Figure 1F can be at either location and may also be duplicated on each platform. Preferably, signal acquisition circuitry 154 is part of electronics platform 207 and data transmission circuitry 158 is present at both electronics platform 207 and at electronics disc 206. Thus, communication to DPD 110 could be either from electronics platform 207 or electronics disc 206. Electronics platform 207 may be anywhere upon the surface of substrate 104. In this embodiment it is located in the center.

[0045] As can be seen in Figure 3B, electronics platform 207 is elevated from the surface of substrate 104 with one or more spacers or platform legs 209. As mentioned previously, a processing chamber may have rather large gradients in temperature and other parameters. In some cases, the most severe processing condition may be at the level of the wafer. Elevating the electronics from the surface of the wafer is another way of isolating the electronics from the harshest processing conditions. The platform 207 and the platform leg(s) 209 are preferably made from a material with similar/compatible characteristics as substrate 104, but could be made of virtually any material. Compatibility may relate to thermal coefficient of expansion, or other mechanical, electrical, or material properties. The distance that platform 207 is elevated from substrate 104 can be tailored depending on the processing condition anticipated to be measured, but is generally from 1mm to 5mm. Platform legs may range in size from 0.05 mm in diameter or width (if not round) to more than 1.0 mm and are preferably of a minimal diameter or width of about 0.05mm diameter to limit heat transfer between the substrate and platform. Signals from the electronic circuitry of platform 207 are transferred to substrate 104 via either a small electrical cable or conductors integral to platform legs 209.

[0046] Figure 4 illustrates PCMD 400, another embodiment of the invention. PCMD 400 is similar to PCMD 300 except that it does not include electronics disc 206.

Electronics platform 207 includes SAC 154 and DTC 158. Power supply 162 is preferably located on platform 207 but may also be located on substrate 104. Cable 108 may extend outside the process chamber to function as an antenna, or external transducer, to support the transceiver by allowing communications which would be prevented from within the closed process chamber containing substrate 104 and electronics platform 207. Cable 108 would thus act as part of data link 112, in transmitting signals, real time or delayed, between DTC 158 and DPD 110. Alternatively, cable 108 can be connected directly to DPD 110, and thus data link 112 would be a wired link.

[0047] Figure 5 illustrates PCMD 500, yet another embodiment of the invention. PCMD 500 lacks the cable 108 of PCMD 400 but is otherwise similar. Accordingly, PCMD 500 communicates wirelessly over data link 112. An antenna is preferably integrated into electronics platform 207 but may also be formed in or upon substrate 104.

[0048] Thus far, in all of the embodiments featuring electronics platform 207, that is, PCMD 300, 400, and 500, the platform has been located in the center of substrate 104. This is because it is important to keep the process condition measuring device properly balanced as it may be spun or rotated by a robot arm. However, thermal balance is also important, as is the balance of many other processing conditions. As mentioned previously, a processing condition may vary greatly throughout the processing chamber. Each different processing condition has its own profile or gradient within the processing chamber. Thus, in order to accommodate these variations it is advantageous to vary the location of electronics platform 207 depending on the processing condition, or to locate more than one platform upon or into the substrate.

[0049] In Figure 6, PCMD 600 has electronics platform 207 located near the edge of substrate 207. Otherwise PCMD 600 is the same as PCMD 500. In Figure 7, PCMD 700 has two or more electronics platforms 207 and 209 located on a diameter of substrate 104 and equally distanced from the center of substrate 104. PCMD circuitry 151 may be divided in any proportion between electronics platform 207 and 209 - including a configuration wherein platform 209 has no electronic components or circuitry. Also, the PCMD circuitry 151 may be duplicated on each platform.

[0050] In any embodiment a platform containing all or part of the PCMD circuitry 151, i.e., memory 152, SAC 154, DTC 158, and power supply 162 may be alternatively integrated into the substrate or contained within a cavity formed within the substrate.

This is done so that the substrate 104 used to measure the processing conditions has substantially the same mass as a production substrate subject to the processing conditions of actual production. The objective is to remove the same mass of substrate as added by the platform in order to accurately simulate the effects on as similar a test substrate (104) as possible. Dynamic thermal response times to temperature changes can be most accurately measured if the mass and thermal conduction within substrate 104 is similar to the product substrate.

[0051] While particular embodiments of the present invention and their advantages have been shown and described, it should be understood that various changes, substitutions, and alterations can be made therein without departing from the spirit and scope of the invention as defined by the appended claims. For example, the location and type of the sensors may be different than in the examples described. Additionally, the electronics platform or disc may be recessed into the a cavity of the measuring substrate, and circuitry that performs the same function in the same way to get the same result is also within the scope of the invention.